STATINTL [redacted]

15 October 1965

PROGRESS REPORT NO. 2

9/1/65 - 9/30/65

REPORT NO. SME-BL-2

UNIVERSAL DATA BLOCK READER

J-3 JTCR - (...)

Warder 1/8"

Hard copy ...

This document has been prepared in accordance with the requirements

STATINTL [redacted]

STATINTL

Submitted By: [redacted]

Program Manager

1.0 SUMMARY

The major emphasis during this period has been placed in the following areas:

a) Generation of the Engineering Change Proposal.

b) Definitization of what formats are to be read, including those introduced by the change in scope.

c) Logic Design.

d) Transport Design.

e) Circuit Design and Breadboard.

Satisfactory progress has been made in each of these areas and is discussed in the appropriate portions of this report.

2.0 PROGRAM SCHEDULE/MILESTONE STATUS

The major milestones completed during this period were:

1) Film Transport Design.

2) Read Amplifier.

3) Magnetic Tape Recorder Procurement.

The major milestone commenced or continued but not completed during this period were:

1) Power Analysis.

2) Core Memory Procurement.

3) Logic Card Components Procurement.

4) Logic Card Cage Procurement.

5) Data Organization Memory Procurement.

6) Transport Parts Fabrication.

7) Head Optics Design.

Milestones scheduled to be started and/or completed during the next report period are:

1) Read Amplifier Components Procurement.

2) Logic Card Components Procurement.

3) Transport Parts Fabrication.

4) Read Amplifier Package Design.

5) Logic Design.

6) Transport Breadboard.

3.0 ENGINEERING CHANGE PROPOSAL

The Engineering Change Proposal has been prepared and will be formally submitted during the early part of the next report period. A

STATINTL meeting was held at [redacted] facility with the customer and the technical aspects of the changes in scope were discussed.

4.0 DEFINITIZATION OF DATA BLOCK FORMATS

In the interest of clarity, enclosed herewith are sketches of the eleven data block formats which the Universal Data Block Reader will be capable of reading , provided that the Engineering Change Proposal is approved.

5.0 LOGIC DESIGN

The logic design is proceeding quite well, with finalization scheduled for the latter part of the next reporting period or the beginning of the following. All ideas concerning the data acquisition and the information flow have been formulated and subjected to rigorous analysis. Detailed logic diagrams are now in the process of being prepared.

6.0 CIRCUIT DESIGN AND BREADBOARD

The main area where extensive circuit design is required is in the read amplifier. The circuit design, detailed analysis, and breadboard tests were completed during this period. The package design of the amplifier card is now in progress. Completion is scheduled for the end of the next report period.

7.0 FILM TRANSPORT

The film transport design was completed during this report period. All detailing has been finalized and parts released for fabrication. Some parts have been received and initial assembly of the breadboard transport was started at the end of this period. Work will continue on the breadboard as parts are received, with the completion of the breadboard scheduled for the end of the next report period or beginning of the following.

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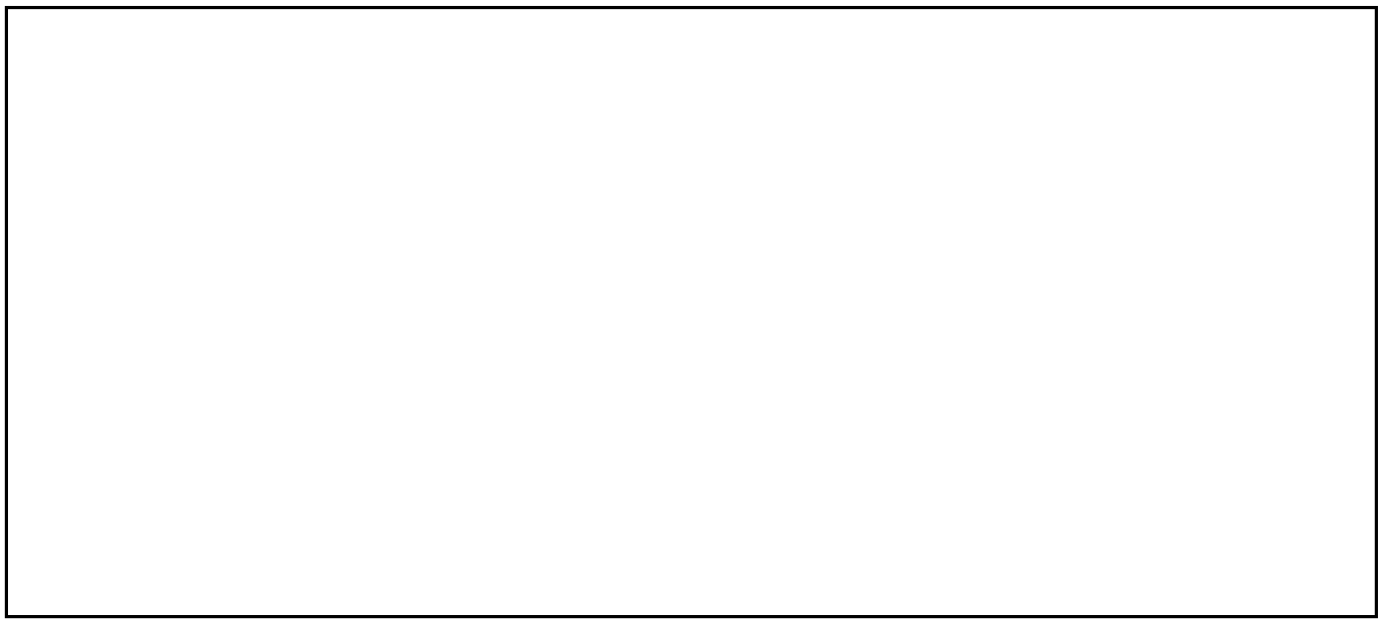

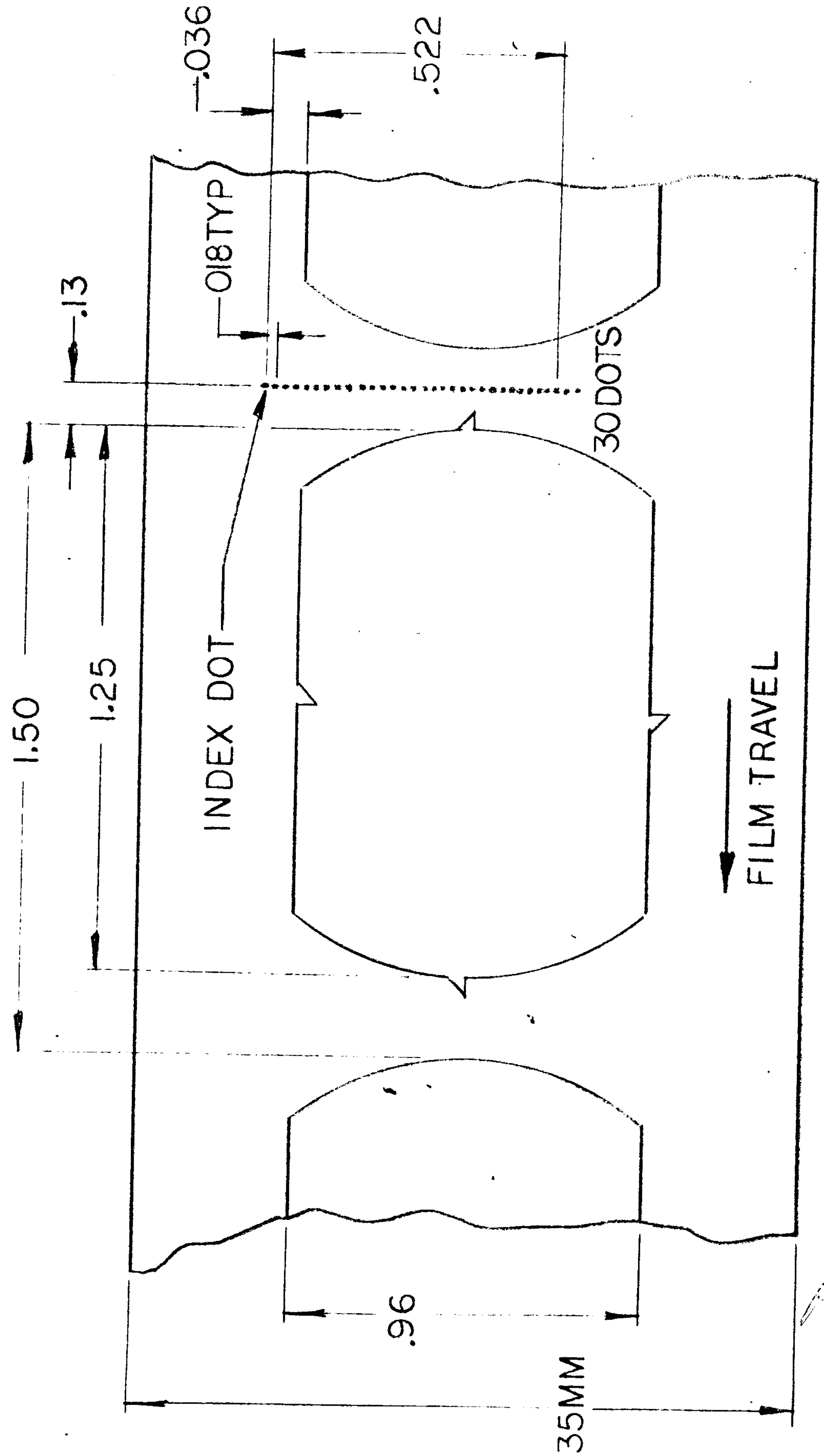

DATA BLOCK TYPE I
35 MM

Proj 57 (FCIC)

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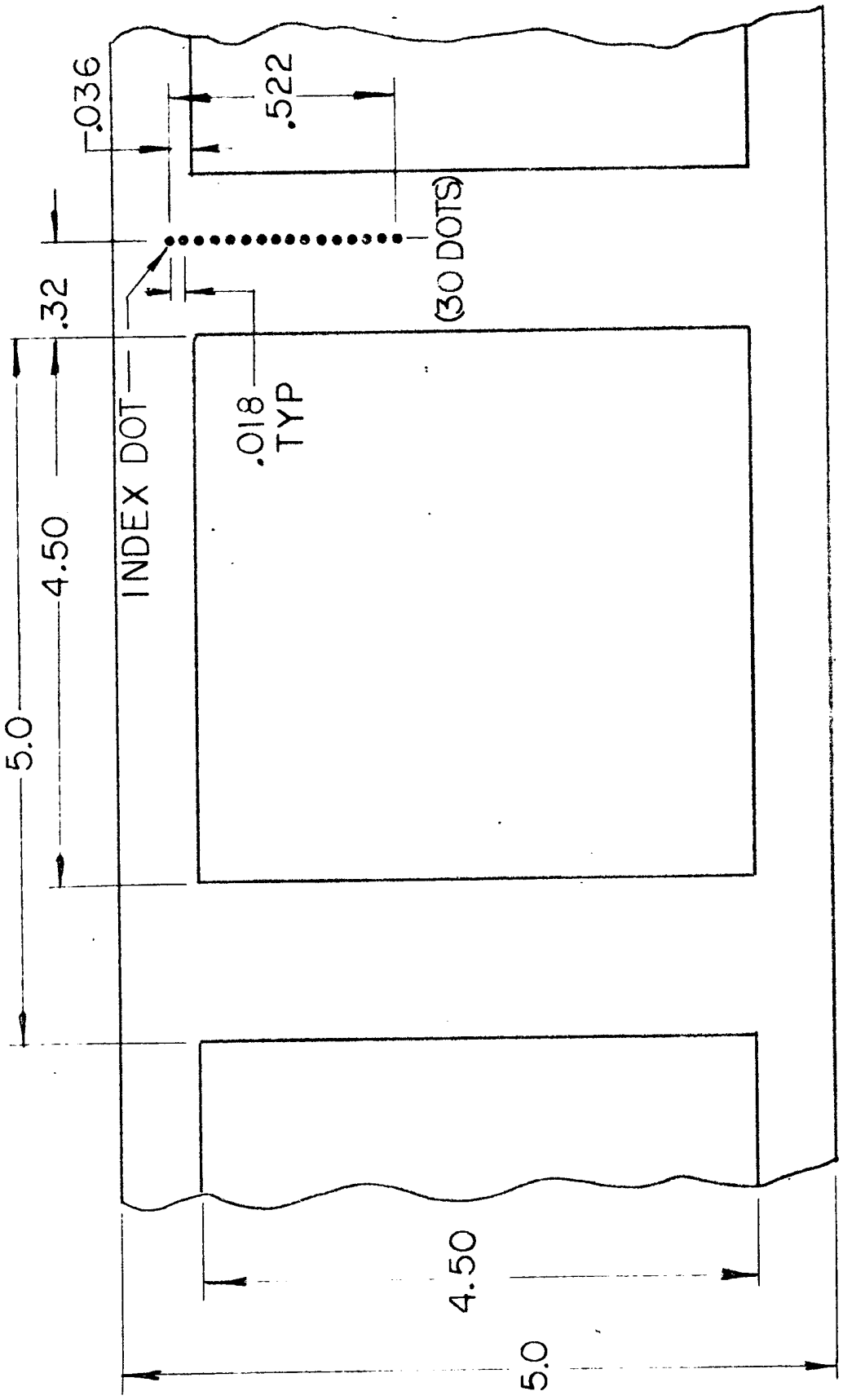
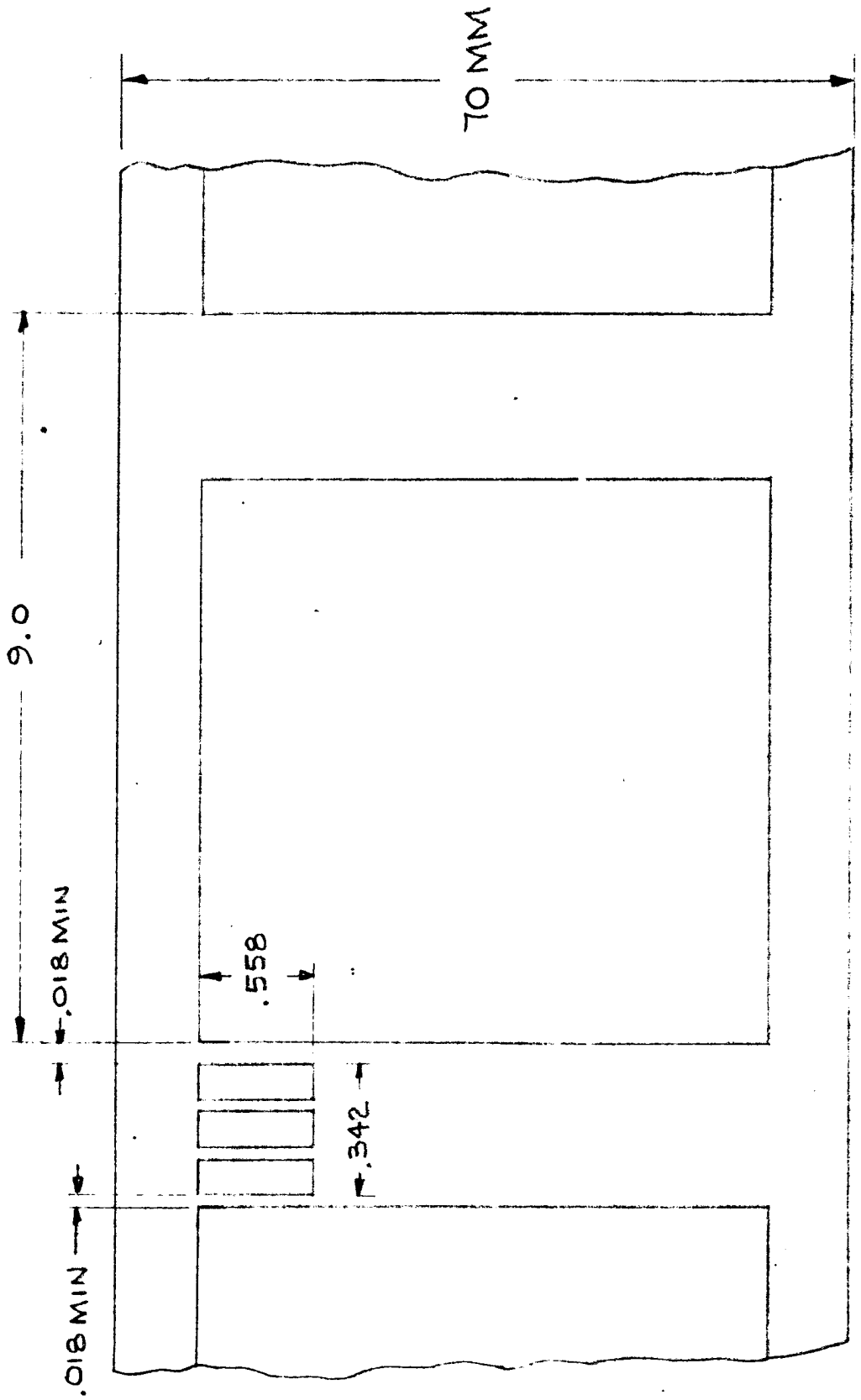


DATA BLOCK TYPE 2
5 INCH

Proj 57
S/I pkg

7-M

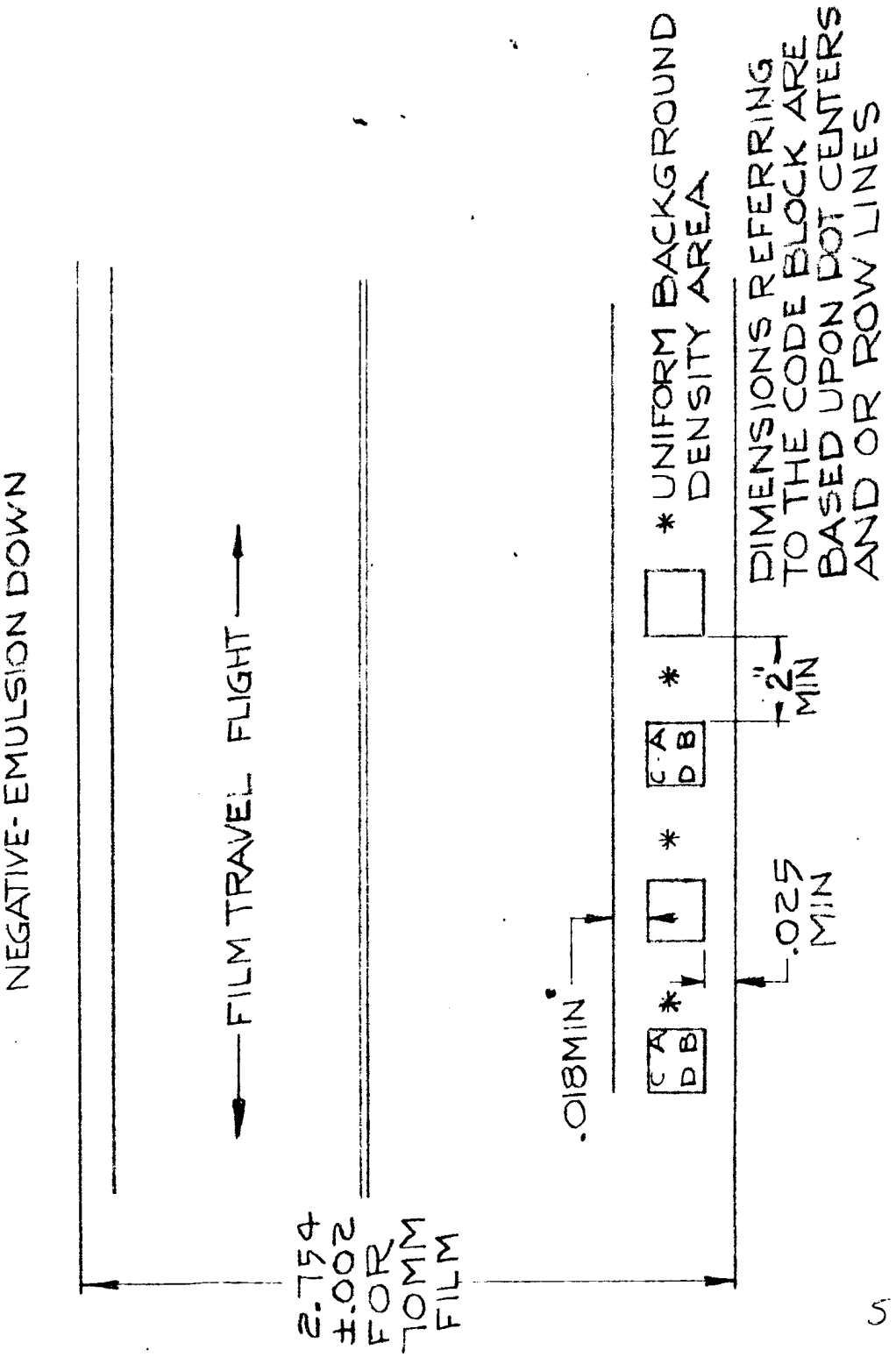


.DATA BLOCK TYPE 3
70 MM

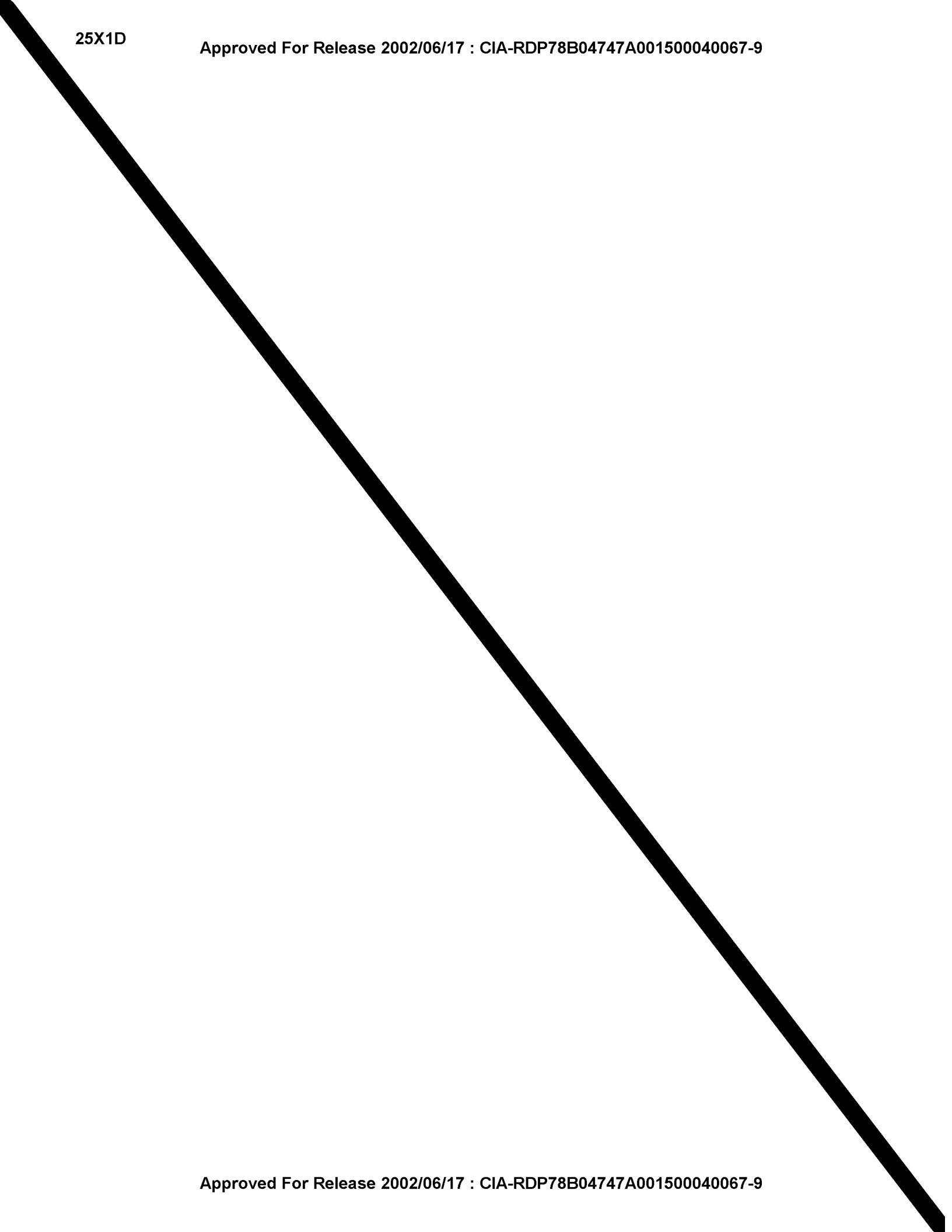
ILLEGIB

SR 11

Old DOD



SLR (SAMPLE)



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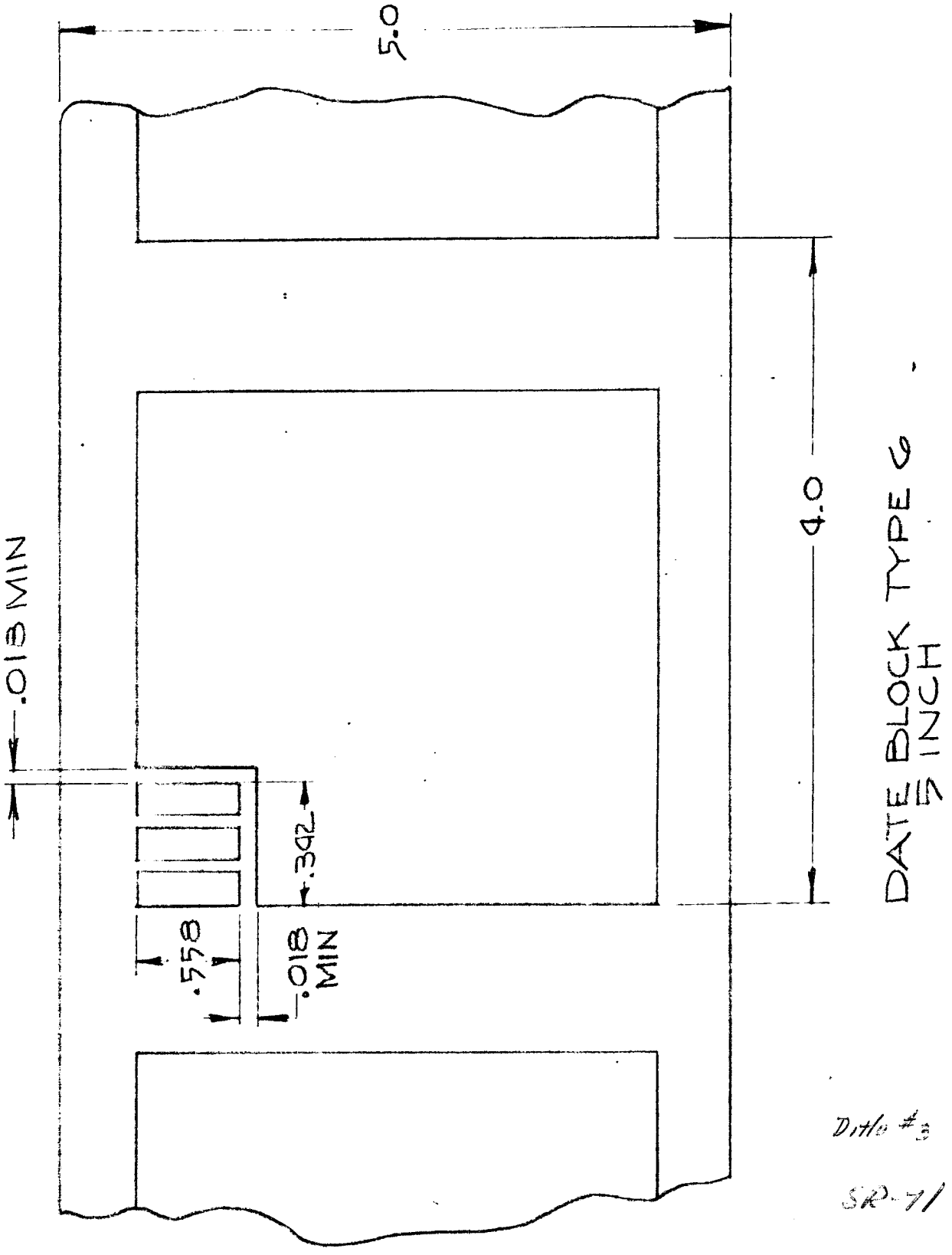
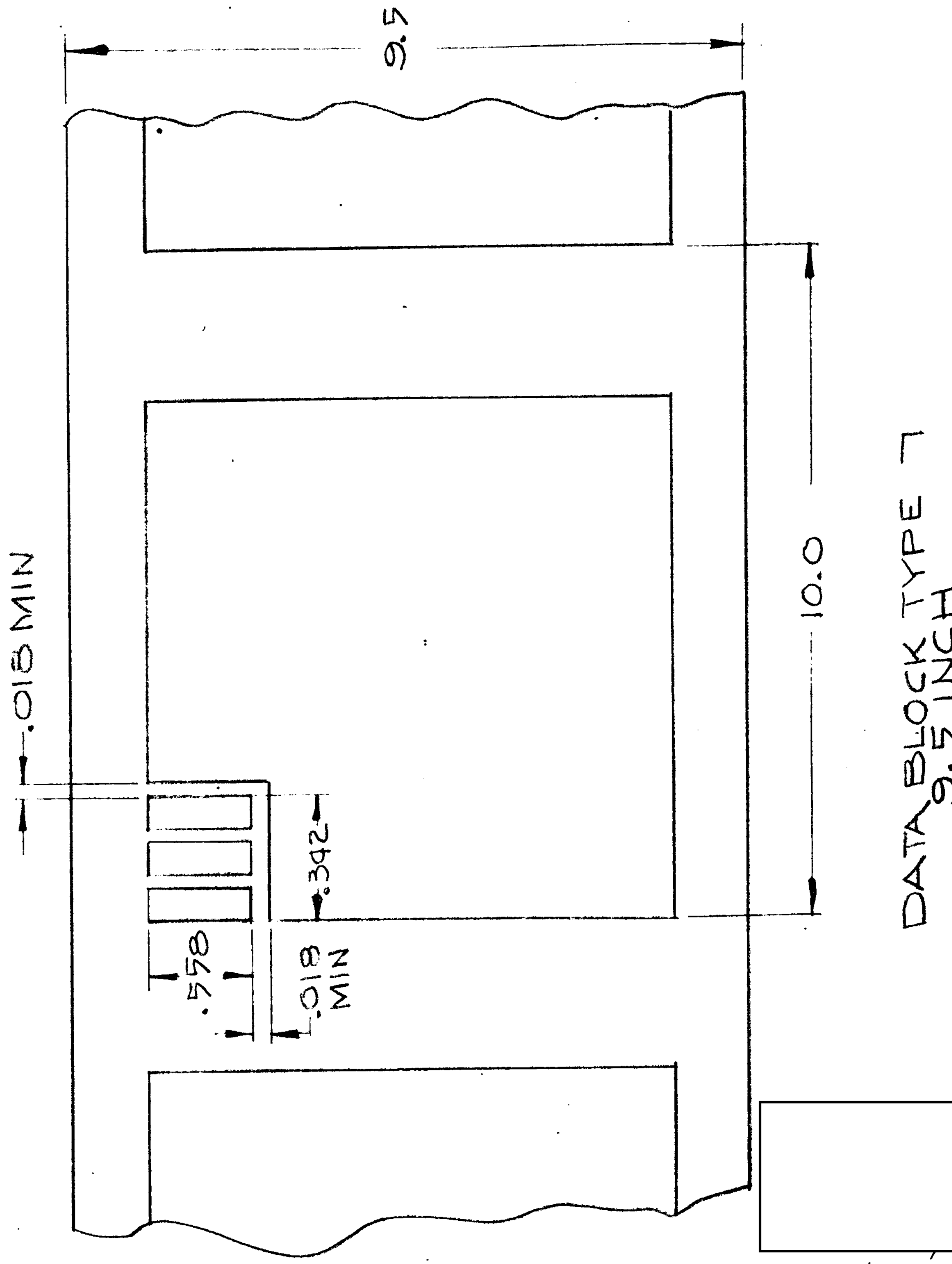


Ditto #3

SR-71

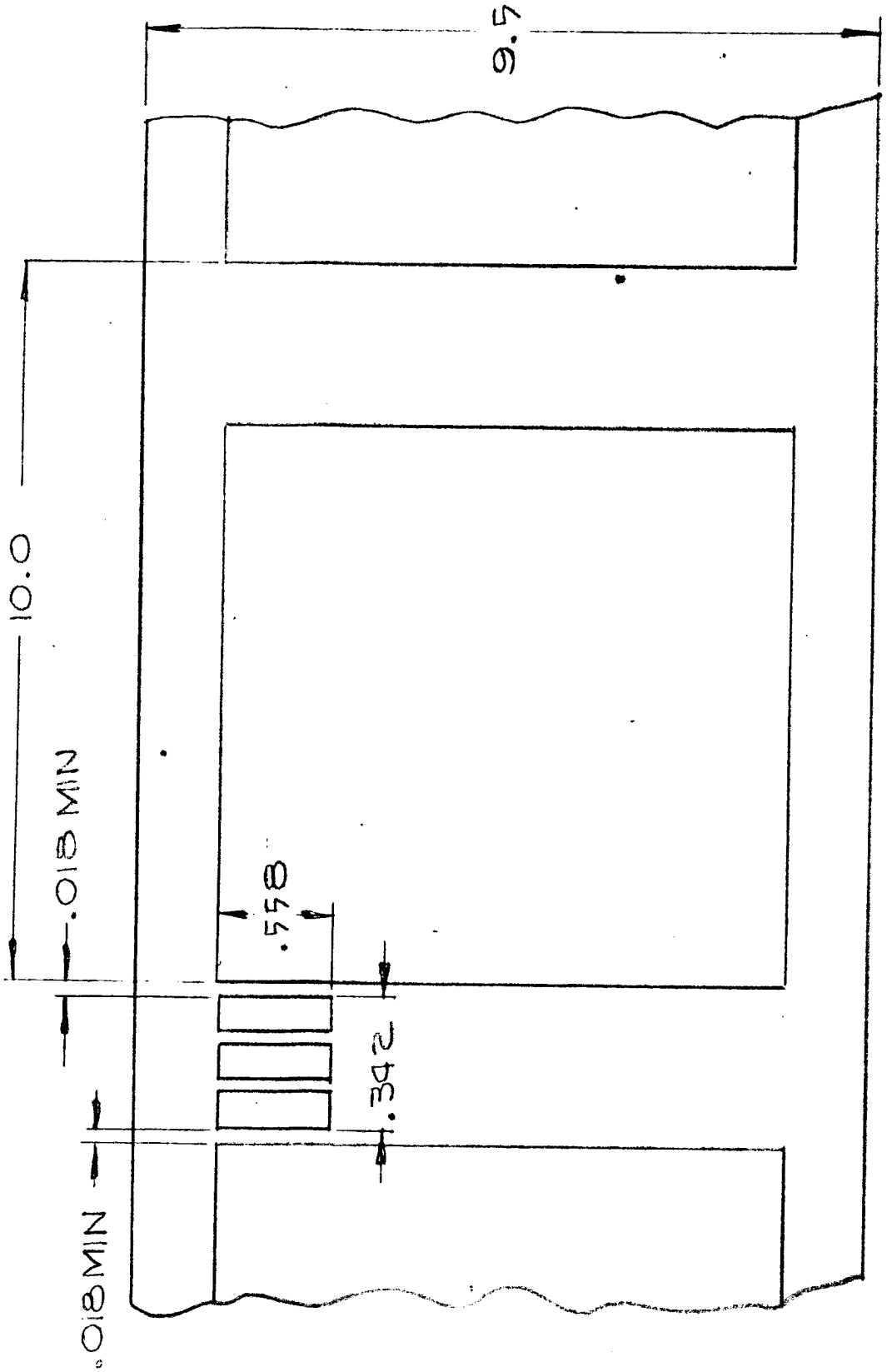
STD DOD



ILLEGIB

SR-71 (F-48?)

STD DOD



DATA BLOCK TYPE B
9.5 INCH

ILLEGIB

CP-71

STD DOD

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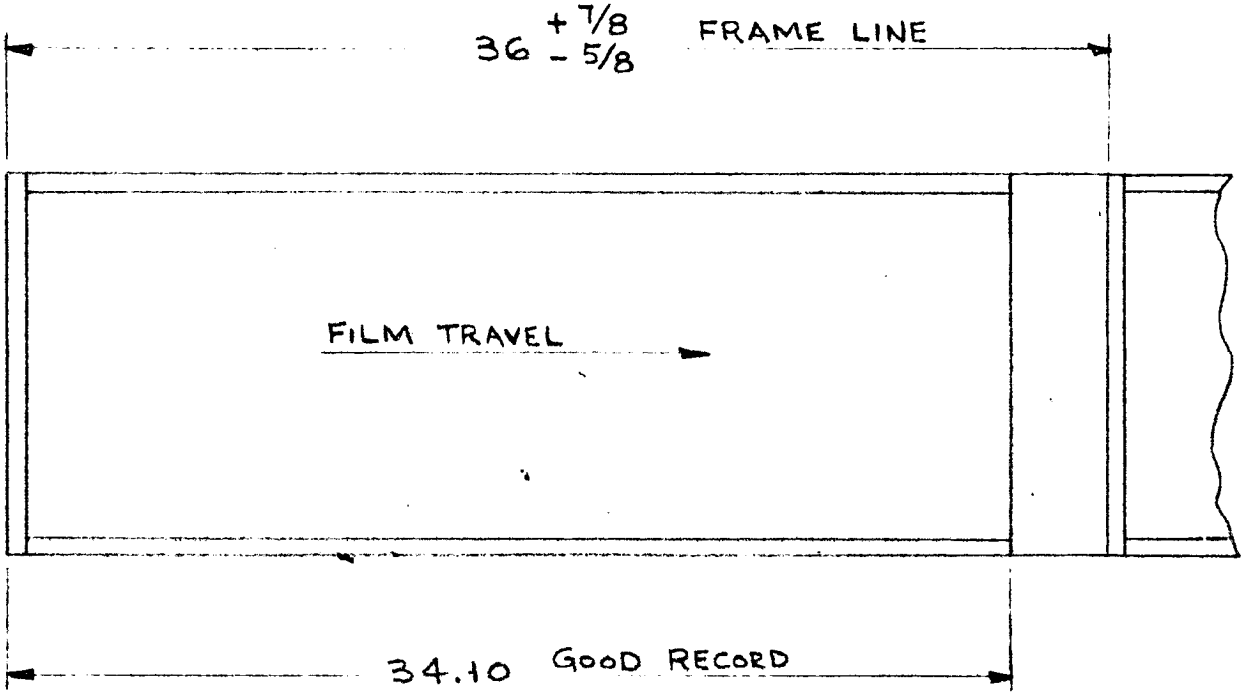

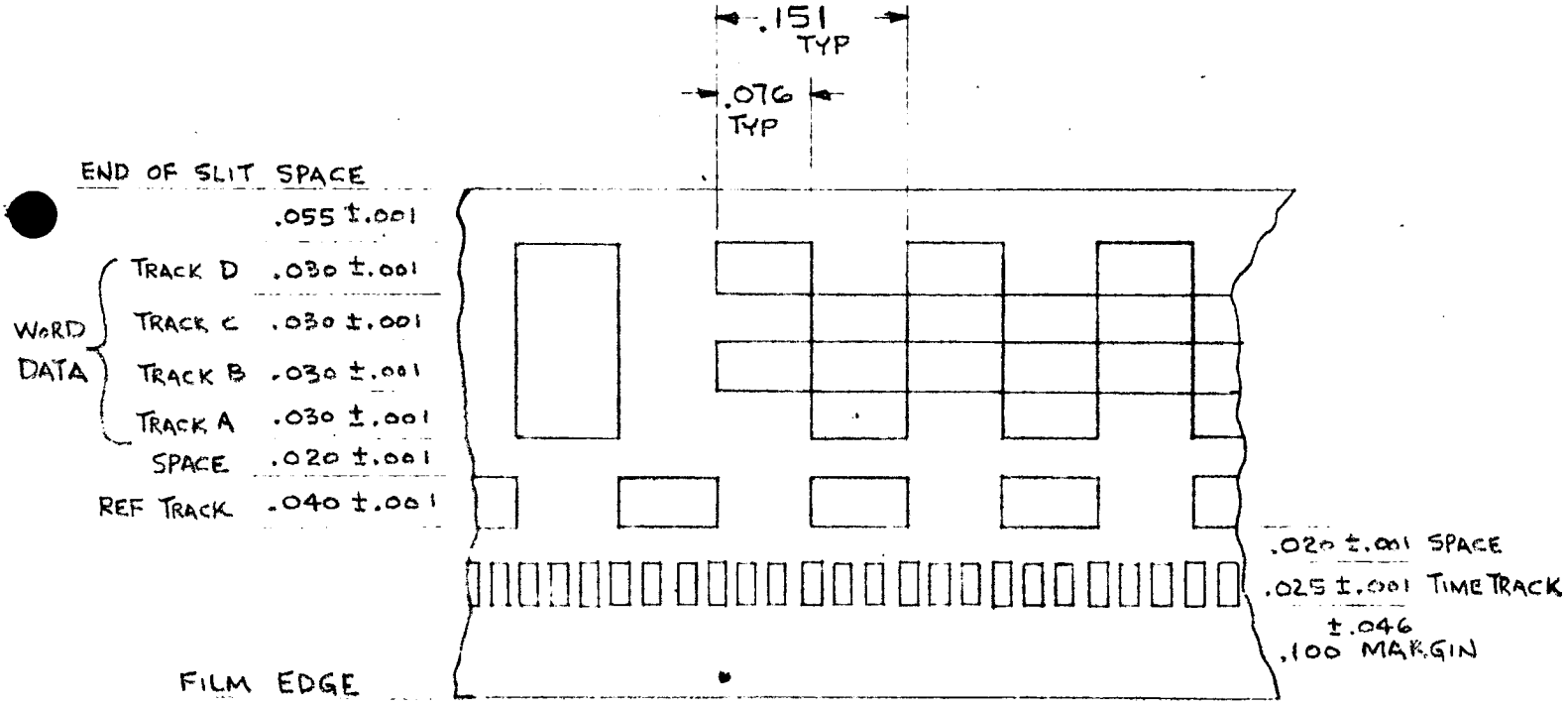

DATA BLOCK TYPE II

116A
Type II